

SiC MOSFET module
application note
2-153A1A Handring instruction

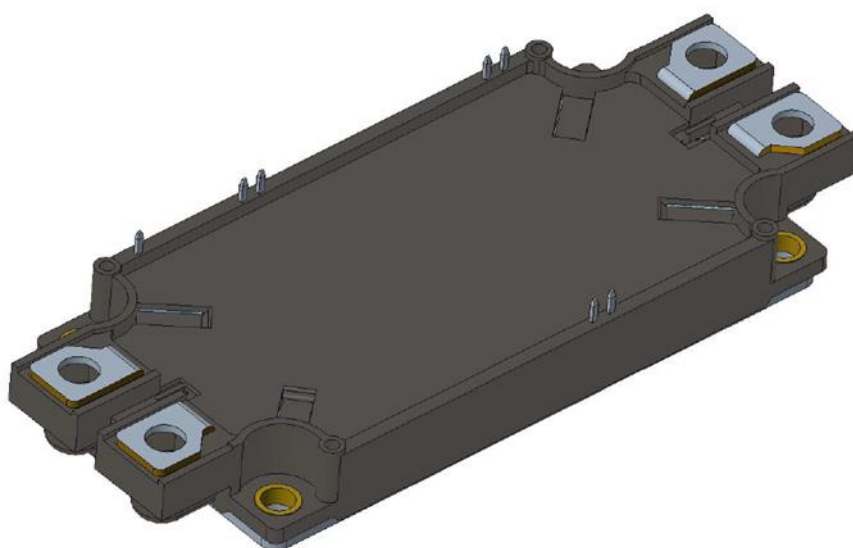


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1. SiC MOSFET module

1.1. Scope

The scope of this application note covers the following products.

Table 1.1.1 Product covered in this application note

Part No.	Voltage rating	Current rating
MG600Q2YMS3	1200V	600A
MG400Q2YMS3	1200V	400A
MG400V2YMS3	1700V	400A
MG250V2YMS3	1700V	250A
MG250YD2YMS3	2200V	250A

1.2. Features of SiC MOSFET Module

Silicon carbide (SiC) is a semiconductor material with a high electric breakdown field strength, saturated electron velocity, and thermal conductivity, compared to silicon (Si). Therefore, when used in semiconductor devices, they achieve higher-speed switching, and lower ON-resistance compared to Si devices for a next-generation low-loss device that contributes to lower power consumption and system downsizing.

The 2-153A1A SiC MOSFET module is equipped with silicon carbide (SiC) MOSFET chips for industrial equipment. This new module meets the needs for high-efficiency, compact equipment for industrial applications such as converters and inverters for railways, and renewable energy power generation systems.

1.3. Internal circuit

The 2-153A1A SiC MOSFET module is a circuit configuration in which two devices are mounted in series in the package. It has a thermistor for temperature sensing. (Fig. 1.3.1)

The thermistor for temperature sensing is installed between terminals 1 and 2. Refer to the datasheet for the thermistor rated resistance and B-value.

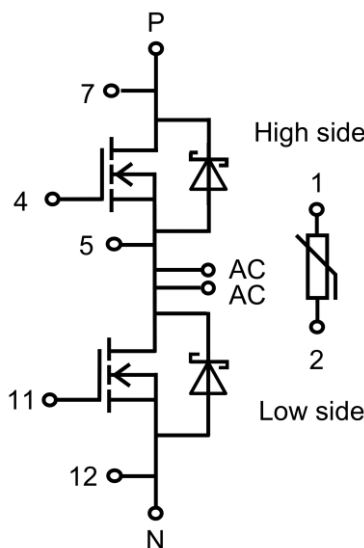


Figure 1.3.1 Internal circuit

1.4. Thermistor

The temperature of the module can be monitored by the thermistor installed in 2-153A1A. Thermistor temperature T can be expressed by the equation (1.4.1) where thermistor resistance value $R_{(T)}$ is calculated using the thermistor B-value and thermistor rated resistance R_{25} as described in the datasheet.

$$R_{(T)} = R_{25} \exp B \left(\frac{1}{T} - \frac{1}{298} \right) \cdots (1.4.1)$$

Since the thermistor is mounted at a distance from the SiC chips and the thermistor itself has a heat capacity, it is not suitable for measuring transient temperature behavior such as short-circuit detection in which the temperature rises over a short period of time.

Use the thermistor within the maximum ratings. The thermistor has a maximum rated voltage of 7.1V, a maximum rated current of 5mA (recommended current of 100μA), a maximum rated power of 10mW, and an operating temperature range of -40°C to 150°C.

Figure 1.4.2 shows an example of the circuit and output voltage (V_{therm}) and thermistor current (I_{therm}) for the thermistor.

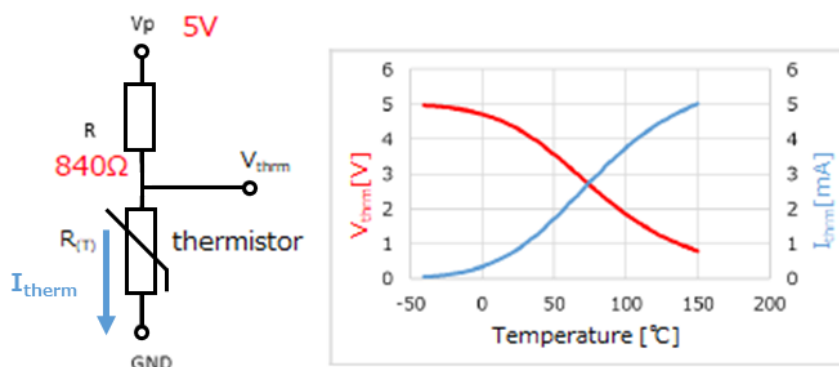


Figure 1.4.2 An example of the circuit and output voltage and thermistor current for a thermistor

1.5. Clearance distance, creepage distance and insulation

The clearance distance and the creepage distance of 2-153A1A are designed to satisfy the values specified by IEC60664-1 applied to an altitude of 2000m or less. Since the required clearance and creepage distances increase with decreasing atmospheric pressure at altitudes of 2000m or higher. When using the product at high altitudes, design the product in accordance with the customer's own responsibility.

1.6. Cosmic ray influence

Similar to silicon power devices, SiC power devices may experience accidental failures due to cosmic rays.

The amount of cosmic rays is affected by latitude and altitude, and is particularly high at high altitude. In addition, cosmic ray breakdown phenomena is more likely to occur at higher operating voltages.

Please contact us if you wish to obtain an estimate of the cosmic ray accidental failure rate when using the product at high altitude or high operating voltage conditions.

2. Transportation, storage and installation

2.1. Transportation

- (1) Due to the weight of the devices, care should be taken when handling them.
- (2) The orientation and maximum number of stacked items should be in accordance with the markings on the packing boxes. (Fig.2.1.1)
- (3) Do not subject the product to impact or drop during transportation, as this may damage the packing box and/or damage the devices.
- (4) Since water may cause malfunction while using the device, be careful not to get them wet especially when transporting in rain or snow.

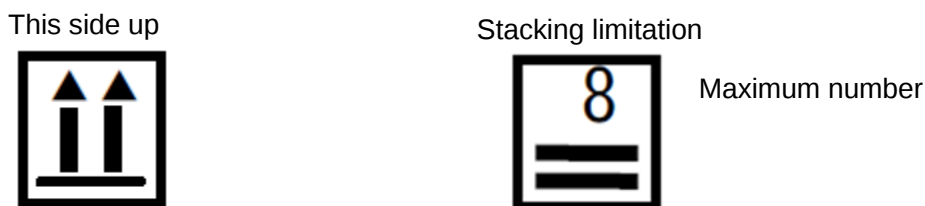


Figure 2.1.1 Signs of orientation and stacking limitation

2.2. Storage

- (1) It is recommended that the device be stored within normal temperature and humidity ranges (temperature: 5 to 35°C, humidity: 45 to 75%).
- (2) Avoid storing the product in an atmosphere containing corrosive gases or organic solvents, etc., or dusty places.
- (3) Cardboard is the main material for the packaging box when the device is delivered, it is not suitable for long-term storage. If the product is to be stored for a long period (1 year or more), consider using different packaging for storage.
- (4) Condensation may form on the surface of the device if the temperature changes suddenly. Avoid this environment by storing in a place with minimum temperature fluctuation.
- (5) Store according to the instruction on the package box. Especially when the packing boxes are stacked as an unexpected load may be applied.

2.3. Anti-static discharge handlings and gate protection

The gate to source voltage has an absolute maximum rating. Check the datasheet for the absolute maximum ratings. If voltage exceeds this gate-source voltage, there is a risk that may cause a gate failure. Be careful not to apply a voltage between the gate and source terminals exceeding the maximum rated voltage stated in the datasheet.

SiC MOSFET gates also require care against electrostatic discharge. Follow these precautions when handling the device.

- (1) Electrostatic charges on the human body and clothing should be discharged with an antistatic wrist strap band with grounding wire, etc., before handling the device.
Work on an anti-static floor mat.
- (2) The device is individually packaged in an antistatic bag. Do not touch the terminals of the device directly when opening the bag. Hold the plastic body. After removal, short-circuit the gate and source terminals.
- (3) When connecting and fixing main electrodes, signal circuit components, etc., take measures against accidental discharging of the materials used to prevent static electricity from being applied to the devices in the same manner as (1).

If voltage is applied to the main circuit when the product is installed, while the gate circuit is faulty or the gate circuit is not operating normally (the gate is open), the device may be damaged. To prevent this destruction, we recommend adding a protection circuit such as short-circuiting between the gate and source when the gate circuit is not powered on, and prevent the main circuit powering on unless the gate bias is negative.

2.4. Mounting on the heatsink

2.4.1. Arrangement of devices

In order to obtain a sufficient heat dissipation effect without applying heat or mechanical stress to the device, make sure the following points when mounting the device to the heatsink.

When attaching the devices to a heatsink, make sure that the plastic body of the devices do not come into contact with each other. Contact may cause damage due to shock, vibration, warping of the heatsink, etc. Check the datasheet for the upper limit of device dimensions.

Figure 2.4.1.1 shows good and bad examples of mounting multiple devices on a heatsink. Consider the heat generation of each module when designing the mounting position on the heatsink.

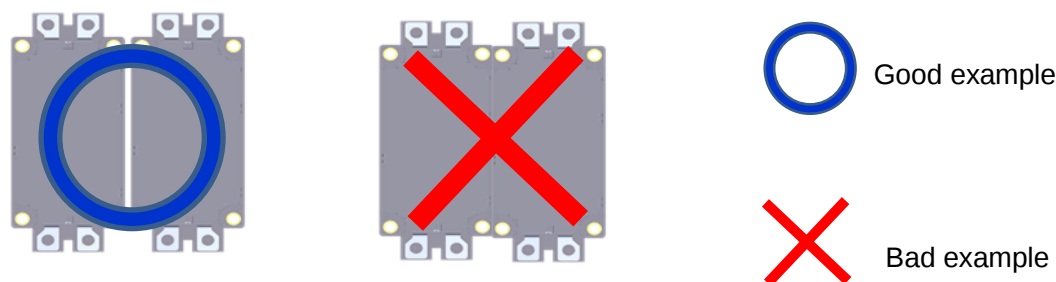


Figure 2.4.1.1 Mounting design on a heatsink

2.4.2. Design of heatsink

Select an appropriate cooling system for power dissipation of the devices.

When using air-cooled or water-cooled heatsinks, make sure that the channel temperature of the device does not exceed T_{ch} .

As shown in Fig. 2.4.2.1, the heatsink should have a flatness of $30\mu\text{m}$ or less in the area of the base plate is attached. The surface roughness should be $10\mu\text{m}$ or less.

Flatness $< 30\mu\text{m}$
(in the area of the base plate is attached)

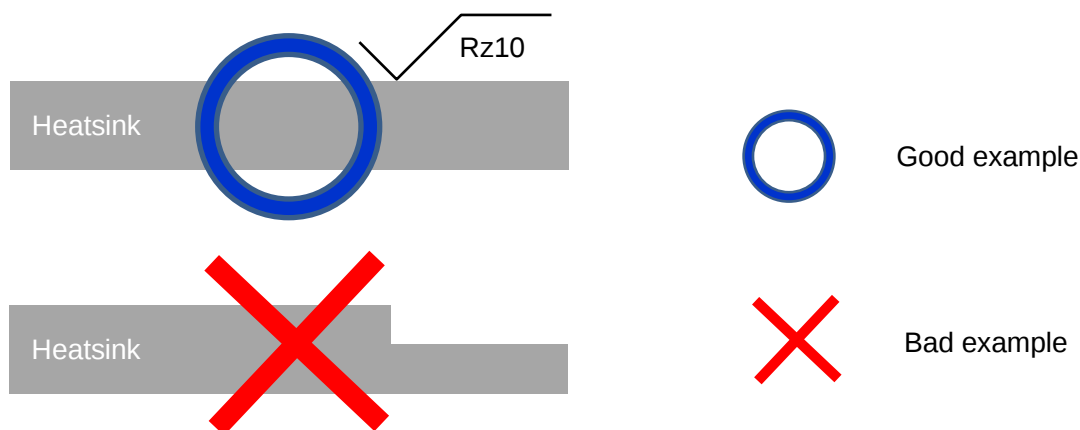


Figure 2.4.2.1 Flatness and surface roughness of heatsink

To obtain a sufficient cooling effect, mount the base plate directly on the heatsink. Use M5 screws for mounting the device to the heatsink. To avoid unbalanced screw loads on the base plate, insert a washer between the screw and base plate, or use a flanged screw. An unbalanced screw load may cause damage.

2.4.3. Application of grease

Apply grease between the base plate and the heatsink to reduce thermal contact resistance between the base plate and the heatsink. Select a non-volatile, long-life grease with high thermal conductivity (which ensures the required thermal conductivity) and apply it thinly and uniformly so that air does not enter between the base plate and the heatsink.

It is important to apply a thin coat of grease to achieve sufficient heat dissipation.

Several types of grease, with different characteristics, from ease of application to difficulty of deterioration of thermal conductivity, are available. Choose the appropriate grease.

2.4.4. Mounting devices

After applying grease, mount the device to the heatsink with the recommended torque described in the datasheet in the order shown in Fig. 2.4.4.1. It is recommended when screwing to the base plate, do so three times (20% of the recommended torque first time (engagement of screws), 60% of the recommended torque second time (prevention of single tightening of multiple screws), and 100% of the recommended torque third time (full tightening)). Tightening below the recommended torque may cause loosening during use. Unbalanced screwing or tightening above the maximum rated torque may cause damage. A manual torque screwdriver is recommended because some motorized torque drivers and air torque drivers instantaneously apply more than the recommended torque.

Clean the screws, screw holes, washers, and base plate surface.

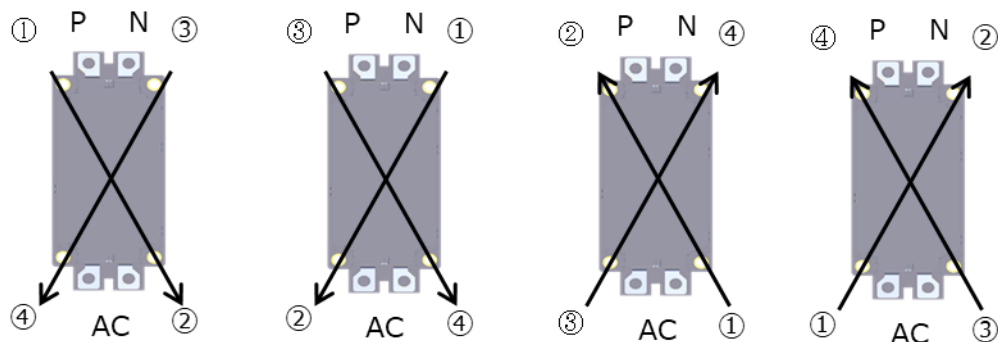


Figure 2.4.4.1 Tightening sequence of screws

2.5. Mounting of main electrodes

Screw and connect the main electrodes to all main terminals (1 P terminal, 1 N terminal, 2 AC terminals) as shown in Fig. 2.5.1. If there is a terminal that is not connected, it may differ from the performance described in the datasheet and may cause damage to the device.

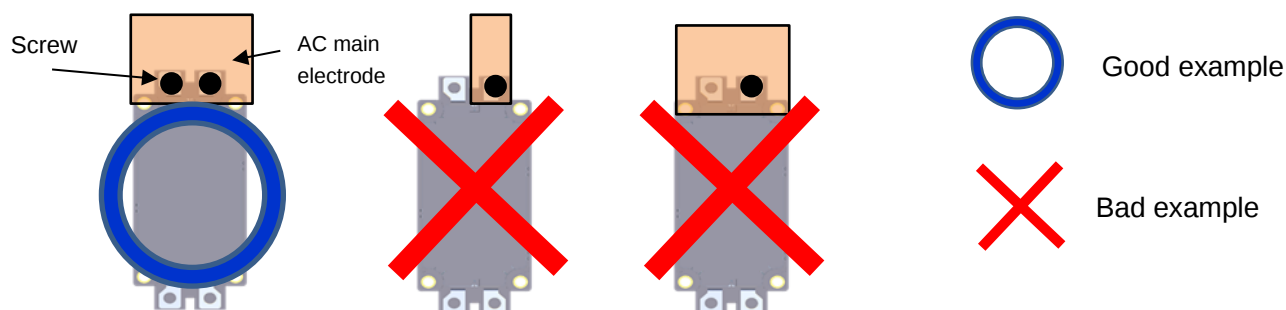


Figure 2.5.1 Connection of terminals and main electrodes (Good and bad examples for AC Terminals)

Design the main electrodes so that current flows equally to each AC terminal as shown in Fig. 2.5.2.

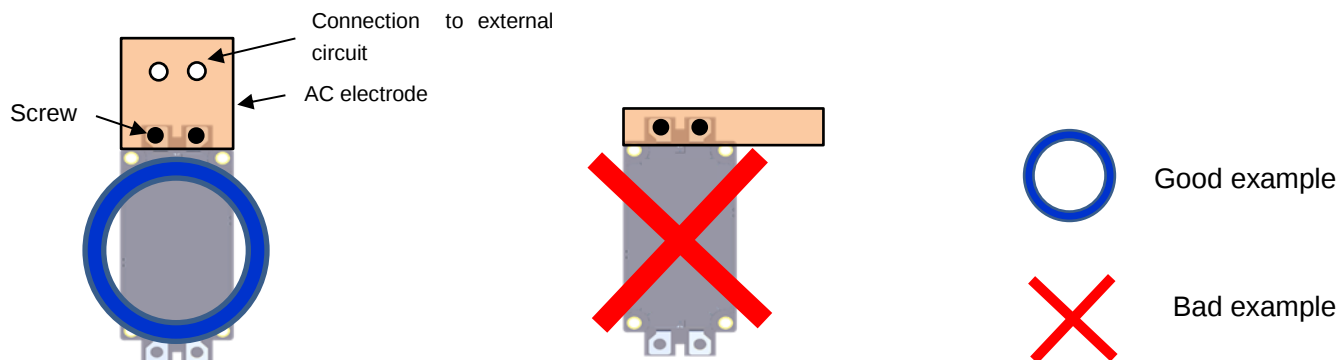


Figure 2.5.2 Examples of design of terminals and main electrodes (Good and bad examples for AC Terminals)

Design the main electrodes so that current flows equally to each P, N, AC terminals when devices are used in parallel. The symmetrical electrodes should be applied as shown in Fig. 2.5.3

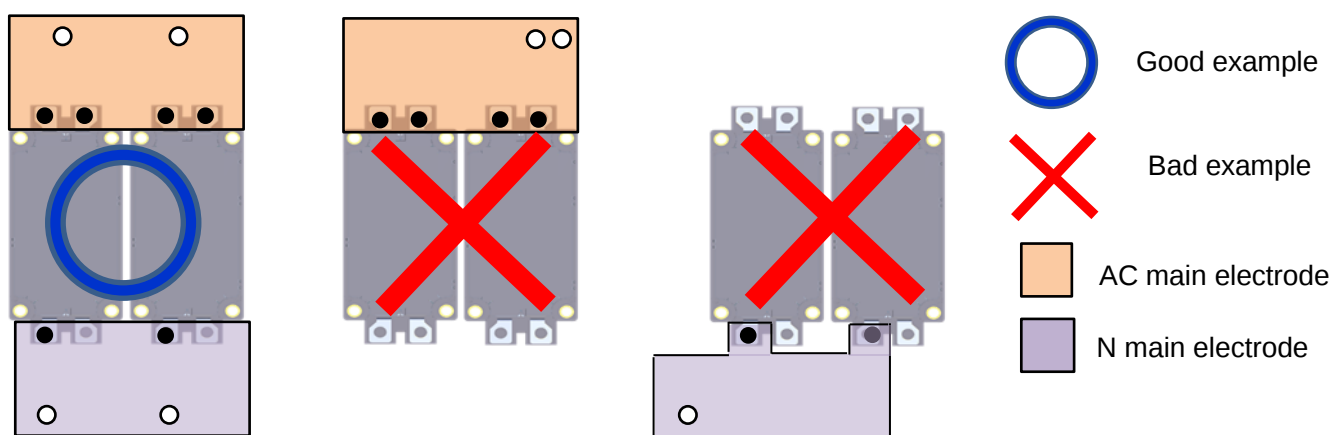


Figure 2.5.3 Examples of design of main electrodes for two devices (Good and bad examples for AC and N Terminals)

Design the main electrodes between the P, N terminal and AC terminal area so that they are not covered by the main electrodes as shown in Fig. 2.5.4. Devices may be affected by electromagnetic induction from the main circuit current.

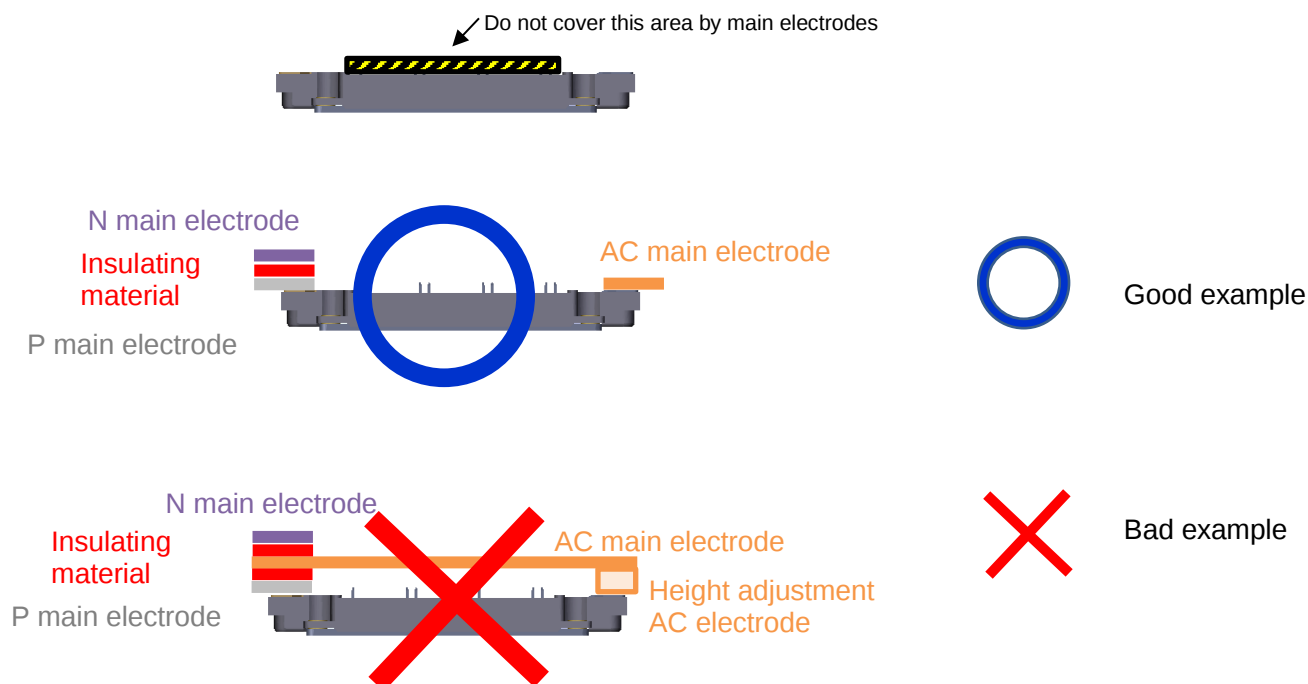


Figure 2.5.4 Design of main electrodes (Good and bad examples for P, N and AC Terminals)

It is recommended that the design the electrodes are parallel and insulated for the P and N main electrodes as shown in Fig. 2.5.5. (Refer to Fig. 2.5.7 for an example when the P and N main electrodes are displaced.)

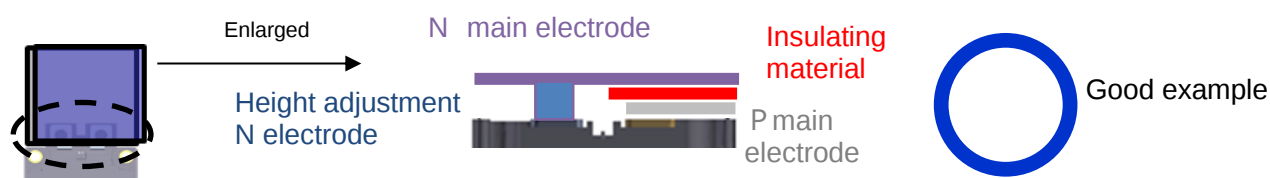


Figure 2.5.5 An example of recommended parallel electrodes and insulation for P and N main electrodes

As shown in Fig. 2.5.6, when the P and N main electrodes are spaced too far apart, the main circuit inductance is increased, so high-speed switching, which is a characteristic of SiC MOSFET, may become difficult.

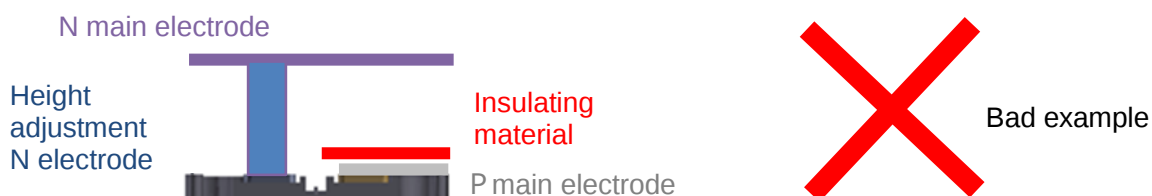


Figure 2.5.6 An example of increase in main circuit inductance by spaced P and N main electrodes

Do not design the main electrodes so that the P-N insulation distance may be insufficient as shown in Figs. 2.5.7 and 2.5.8. Design the main electrodes to satisfy the spatial distance specified by the customer.

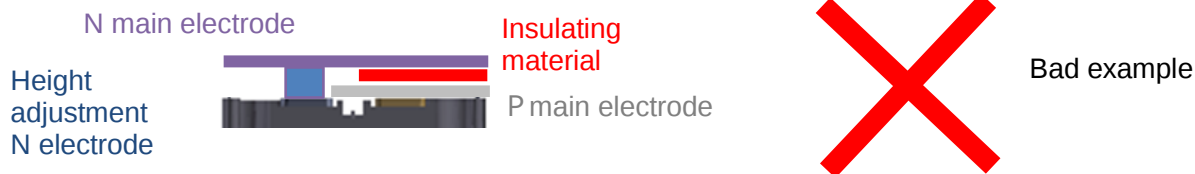


Figure 2.5.7 An example of insufficient insulation distance between P and N terminals, in the case that the P main electrode moves toward the N main electrode

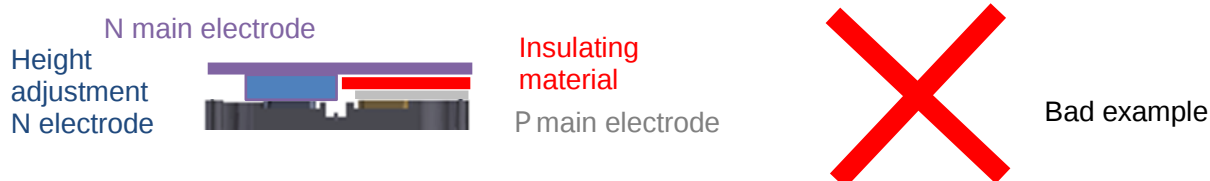


Figure 2.5.8 An example of insufficient insulation distance between the P and N terminals with height adjustment N electrode (This figure shows an example of the N electrode, the same applies to the P electrode)

When mounting the main electrodes to the device, make sure that a load exceeding Fig. 2.5.9 is not applied to the terminal. This load is the maximum allowable value only once when mounting the main electrodes.

After tightening the main electrodes and terminals (P, N, and AC terminals), it is recommended that the main electrodes be designed so that no force is applied to pull the main electrode in the +Fz direction as shown in Fig. 2.5.9.

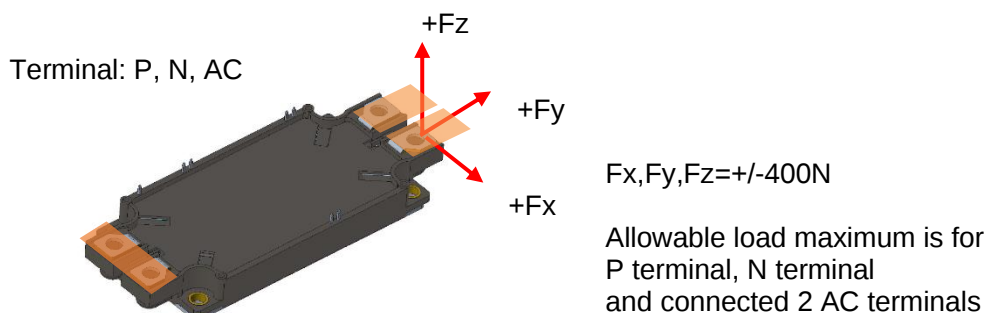


Figure 2.5.9 Maximum allowable load when mounting main electrodes for P, N, and AC terminals

Select the mounting screws that will not exceed the maximum length from the top surface of the terminals described in Table 2.5.10. Using screws longer than this may damage the device.

Table 2.5.10 Length of the mounting screw from the top surface of the main terminals

Terminal name	Length of the mounting screw from top of the mail terminals	
P,N,AC	maximum length 12.8mm	minimum length 8mm

2.6. Installation of Signal Circuit Components

2.6.1. Design of signal circuit

It is recommended to design the signal circuit on a circuit board.

Design the signal circuit components so that they are not affected by external electromagnetic induction.

Design the signal circuit components so that there are no electrically floating terminals.

Example: When a thermistor is not used, pin 1 and 2 should have the same electrical potential as pin 12.

2.6.2. Mounting of circuit board

When mounting the circuit board to the device tighten the screws before soldering the signal terminals. Tighten the screws by manual torque screwdriver. Do not tighten repeatedly.

Use tapping screws designed for resin. Select screws that do not exceed the maximum length listed in Table 2.6.2.1. Using a screw longer than this may damage the device.

Appropriate screws and tightening conditions differ depending on the environment, so be sure to evaluate the actual device before determining the screwing condition.

Table 2.6.2.1 Nominal diameter and maximum length of circuit board mounting screws

Recommended nominal diameter of tapping screws for mounting circuit board	Maximum length of tapping screws for mounting circuit board
2.5 to 2.6mm	12mm (circuit board thickness 1.6 to 2mm)

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