

ADC_SINGLE

1. Operation Outline

The AD conversion of analog input values generated by externally controlled variable resistor is executed one time.
Then the result is displayed on the terminal software.

2. Each Setting

<u>UART</u>	: TXD2 (Port93) : RXD2 (Port94)	
<u>AD operating mode</u>	: Single mode	
<u>Analog input channel selection</u>	: AIN7 (Port47)	Input externally
<u>AD conversion time</u>	: default = 156/fcgck	main.c: Changing the value of "#define ADC_SINGLE_TIME" allows changing the AD conversion time.
<u>Serial port setting</u>	Baud rate Data Parity Stop Flow control	: 115200 (bps) : 8 (bit) : None : 1 (bit) : None

3. Basic Operation

The AD conversion of input values from the analog input channel is executed only one time with ADC.
Then the values of the AD operating mode, AD conversion time, and AD conversion result are displayed on TeraTerm.
The result values are shown as follows:

10bit AD Converter Sample Program

```
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Operating mode: Single  
Conversion time select: 156/fcgck  
AD converted value:  
0x29
```

4. Note

None.