

ADC_REPEAT

1. Operation Outline

The AD conversion of analog input values generated by externally controlled variable resistor is executed 10 times.
The result is displayed on the terminal software every 1 second.

2. Each Setting

UART : TXD2 (Port93)
: RXD2 (Port94)

AD operating mode : Repeat mode

Analog input channel selection : AIN7(Port47) Input externally

AD conversion time : default = 312/fcgck main.c: Changing the value of "#define ADC_REPEAT_TIME" allows changing the AD conversion time.

Serial port setting Baud rate : 115200 (bps)
Data : 8 (bit)
Parity : None
Stop : 1 (bit)
Flow control : None

3. Basic Operation

The AD conversion of input values from the analog input channel is executed 10 times with ADC.
Then the values of the AD operating mode, AD conversion time, and AD conversion result are displayed on TeraTerm.
The result values are shown as follows:

```
10bit AD Converter Sample Program
```

```
-----  
Operating mode: Repeat  
Conversion time select: 312/fcgck  
AD converted value:  
0x029  
0x111  
0x02B  
0x13C  
0x3FF  
0x27F  
0x004  
0x123  
0x2AB  
0x3CF
```

4. Note

Nothing.