

TC0_12bit_PWM

1. Operation Outline

Whenever SW is input, the Duty ratio is changed.

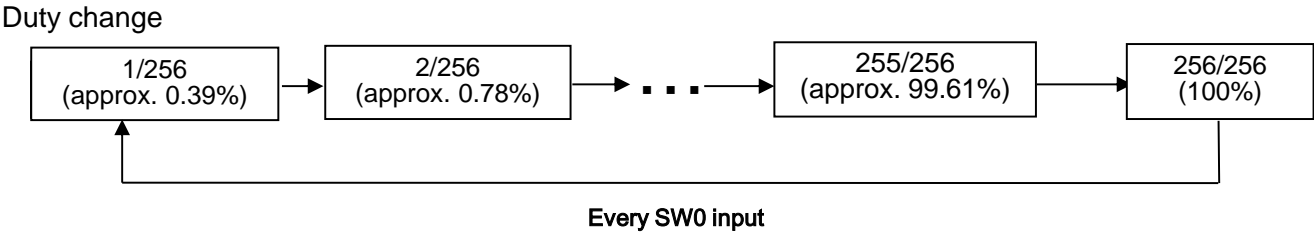
2. Each Setting

<u>SW</u>	: SW0 (Port12)	
<u>Timer register</u>	: default = 0x0010-0x0FF0	main.c: Changing the values of "#define TC0_12BIT_PWM_DUTY_MIN" and "#define TC0_12BIT_PWM_DUTY_MAX" allows changing the minimum and maximum Duty.
<u>Operation clock</u>	: default = fcgck/2	main.c: Changing the value of "#define TC0_12BIT_PWM_CLK" allows changing the operation clock.
<u>Timer interrupt/ PWM output</u>	: INTTC01/ PWM01 (Port71)	Cascade connection between TC00 and TC01

3. Basic Operation

Function	Description
PWM output	PWM output setting of Timer function

SW input process
Duty change



4. Note

None.